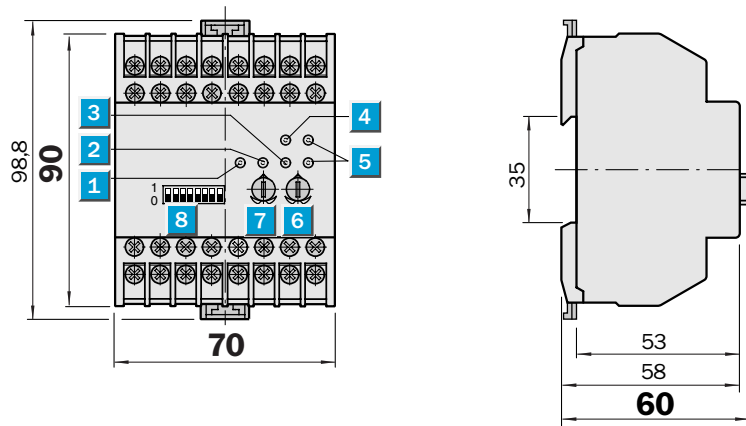


## EN 2 Switching units

### Features

- Universal supply voltage
- 3 inputs that can be connected via DIP-switches
- Adjustable time stages
- Housing with snap fastening for support rail DIN 46277

### Dimensional drawing

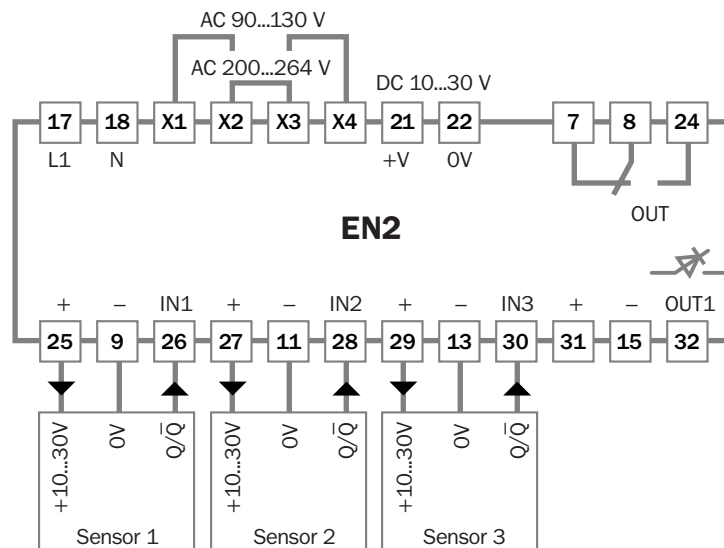


- 1 Display IN 1
- 2 Display IN 2
- 3 Display IN 3
- 4 Operating display
- 5 Display OUT (transistor/relay)
- 6 Time delay off  $t_2$
- 7 Time delay on  $t_1$
- 8 DIP switches F1–F8



### Connection diagram

EN 2  
EN 2T

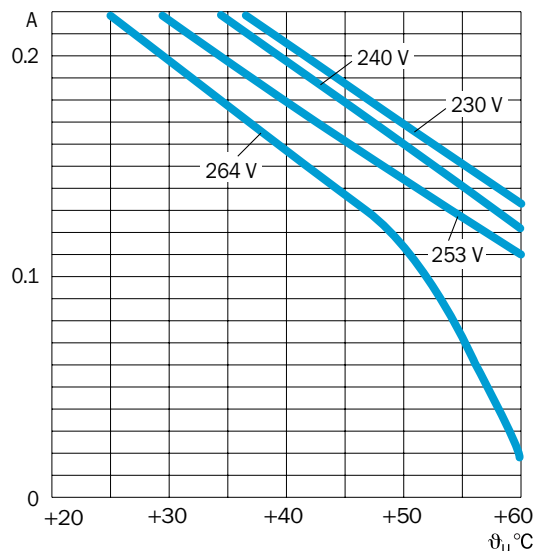


Technical data		EN 2	EN 2T
<b>Supply voltage <math>V_s</math></b>	90...130 V AC or 200...264 V AC (can be reset using bridges) 10...30 V DC <sup>1)</sup>		
Mains frequency	48...62 Hz		
Power consumption	Approx. 40 VA		
<b>Outputs</b>			
Supply voltage for sensor	24 V DC $\pm$ 25 %		
Output current (total)	220 mA (1.4 A) in total, see load curve		
<b>Transistor output</b>			
Switching current, max.	100 mA, short-circuit-resistant, display OUT, flashes on overload, goes out if there is a short-circuit		
Switching frequency	10 kHz		
<b>Relay output</b>			
Switching voltage, max.	250 V AC		
Switching current, max.	2 A		
Switching frequency	10/s		
<b>Inputs</b>			
	26 (IN 1) and 28 (IN 2) and 30 (IN 3) suitable for PNP, NPN <sup>2)</sup> and B sensor outputs		
Input voltage	10...30 V DC		
HIGH	> 10 V DC		
LOW	> 6 V DC		
Minimum switching time	5 $\mu$ s		
<b>Logic</b>			
	Linking the 3 inputs and delay and storage modes via DIP-switches F1 – F8 (see truth table and function diagrams)		
<b>Time stages</b>			
Time delay off $t_1$	0.005...1 s, adjustable		
Time delay on $t_2$	0.005...1 s, adjustable		
	1...120 s, adjustable		
<b>VDE protection class</b>	<input type="checkbox"/>		
<b>Enclosure rating</b>	IP 20		
<b>Ambient temperature</b>	Operation -25 °C...+55 °C Storage -40 °C...+70 °C		
<b>Shock load</b>	Complying with IEC 68		
<b>Weight</b>	Approx. 400 g		
<b>Housing material</b>	Plastic		

<sup>1)</sup> Delivery 200...264 V AC

<sup>2)</sup> External pull-up resistor  $\leq$  10 k $\Omega$   
required for NPN variant

**Load curve – output current**



Order information	
Type	Order no.
EN 2	6 009 654
EN 2T	6 010 342

**Truth table**

<b>F 3</b>	0	IN 1 normal	
	1	IN 1 inverted	
<b>F 7</b>	0	IN 2 normal	
	1	IN 2 inverted	
<b>F 5</b>	0	f (IN 1, IN 2) = IN 1 or IN 2	
	1	f (IN 1, IN 2) = IN 1 and IN 2	
<b>F 8</b>	0	<b>F 8 = 0</b>	
	1	<b>F 8 = 1</b>	
<b>F 6</b>	0	f (IN 1, IN 2, IN 3) = IN 3 or f (IN 1, IN 2)	
	1	f (IN 1, IN 2, IN 3) = IN 3 and f (IN 1, IN 2)	
<b>F 4</b>	0	OUT 1 normal	
	1	OUT 1 inverted	
<b>F 1</b>	0	<b>Mode 1</b> (no delay)	
	0	<b>Mode 2</b> (switching on and switching off delay)	
<b>F 2</b>	1	<b>Mode 3</b> (dynamic delay)	
	1	<b>Mode 4</b> (frequency discriminator)	
<b>F 4 = 0</b>	<b>Mode 5</b>		<b>Mode 9</b>
	<b>Mode 6</b>		<b>Mode 10</b>
<b>F 4 = 1</b>	<b>Mode 7</b>		<b>Mode 11</b>
	<b>Mode 8</b>		<b>Mode 12</b>

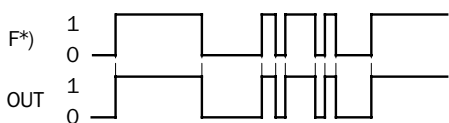
**Logic module:**

The logic input levels of the 3 inputs are linked together depending on the position of the 8 DIP switches F 1 – F 8 on the front of the equipment and immediately, or with a delay, produce a reaction at the output (which is formed in parallel as a relay and semiconductor output).

**Function diagrams and description of the modes**

**F 8 = 0**

**Mode 1**    F 1 = 0    F 2 = 0

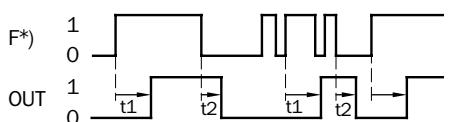


**Delay modes**

**No delay**

Output OUT follows the logic linking of the inputs IN 1, IN 2 and IN 3 F\*) without delay.

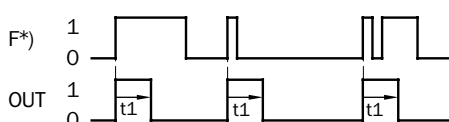
**Mode 2**    F 1 = 0    F 2 = 1



**Delay switching on and switching off**

Output OUT follows with a delay in response and drop-out time F\*). F\*) must be high for a minimum of t<sub>1</sub> for OUT to react. If F\*) is LOW, t<sub>1</sub> is reset. When t<sub>1</sub> elapses, OUT responds, the oscillator is stopped for t<sub>1</sub>. If then F\*) is low again, t<sub>2</sub> begins to run, after the end of which OUT becomes inactive. If F\*) goes high again during t<sub>2</sub>, t<sub>2</sub> is reset and begins to run again at F\*) = LOW. Both times t<sub>1</sub> and t<sub>2</sub> can therefore be retriggered.

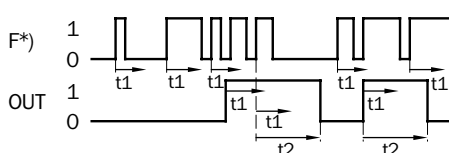
**Mode 3**    F 1 = 1    F 2 = 0



**Dynamic delay**

Output OUT is set with a rising signal edge of F\*) for the duration of t<sub>1</sub>. This time cannot be retriggered here.

**Mode 4**    F 1 = 1    F 2 = 1



**Frequency discriminator**

The time stage t<sub>1</sub> is set by the first rising signal edge of F\*). If a further rising signal edge of F\*) occurs within the course of t<sub>1</sub>, OUT is set for the duration of t<sub>2</sub> and t<sub>1</sub> is retriggered simultaneously. All further rising signal edges of F\*), if they arrive within the course of t<sub>1</sub>, retrigger both t<sub>1</sub> and t<sub>2</sub>. In practice, t<sub>2</sub> should always be set greater than t<sub>1</sub>. If t<sub>1</sub> has elapsed but t<sub>2</sub> has not yet done so, t<sub>2</sub> is not retriggered by the next rising signal edge of F\*).

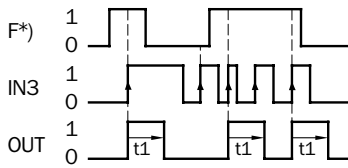
This function produces a frequency discriminator for the setting t<sub>2</sub> > t<sub>1</sub>: If the duration of the period T of the input frequency of F\*) is less than t<sub>1</sub>, OUT always goes to HIGH; if T is or becomes greater than t<sub>1</sub> OUT remains or becomes LOW.

F\*) logic link of the inputs: F = f (IN 1, IN 2, IN 3)

**F 8 = 1**      **F 4 = 0**

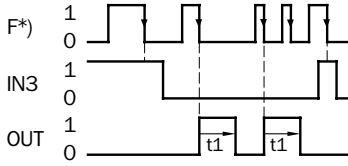
**Mode 5**

**F 1 = 0**      **F 2 = 0**



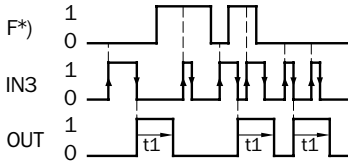
**Mode 6**

**F 1 = 0**      **F 2 = 1**



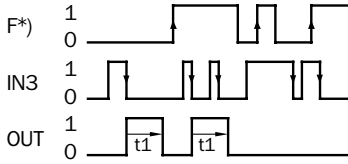
**Mode 7**

**F 1 = 1**      **F 2 = 0**



**Mode 8**

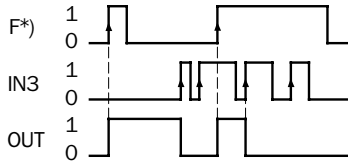
**F 1 = 1**      **F 2 = 1**



**F 8 = 1**      **F 4 = 1**

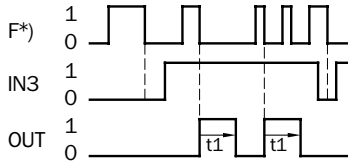
**Mode 9**

**F 1 = 0**      **F 2 = 0**



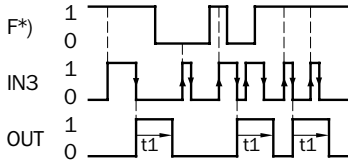
**Mode 10**

**F 1 = 0**      **F 2 = 1**



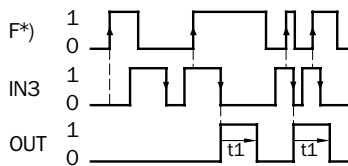
**Mode 11**

**F 1 = 1**      **F 2 = 0**



**Mode 12**

**F 1 = 1**      **F 2 = 1**



**Memory modes**

If during the rising signal edge of IN 3, the link F\*) is HIGH, the output OUT is set with this signal edge for the time  $t_1$ .

If during the falling signal edge of F\*), IN 3 is not HIGH, the output is set for the duration of  $t_1$ .

If F\*) was not HIGH during the rising signal edge of IN 3, the output is set for the time  $t_1$  with the falling signal edge of IN 3.

If no rising signal edge of F\*) occurs during the HIGH time of IN 3, the output is set for the duration of  $t_1$  by the falling signal edge of IN 3.

**Memory modes**

A rising signal edge of F\*) sets the output; a rising signal edge of IN 3 resets it (signal edge-controlled RS-flipflop).

If IN 3 is HIGH during the falling signal edge of F\*), the output is set for the duration of  $t_1$  (i.e. as mode 2, but used inverted in IN 3).

If F\*) was not HIGH when the signal edge of IN 3 was rising, as the signal edge of IN 3 falls, the output is set for the time  $t_1$  (as mode 3, F\* used inverted).

If no rising signal edge of F\*) occurs during the HIGH time of IN 3, the output will be set for the duration of  $t_1$  during the falling signal edge of IN 3.

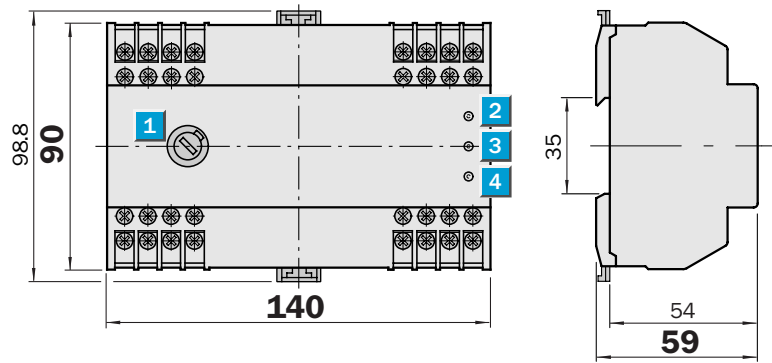
F\*) logic link of the inputs:  $F = f(\text{IN } 1, \text{IN } 2, \text{IN } 3)$

## EN 3 Switching units

### Features

- Universal supply voltage
- 2 inputs, each with a relay output
- Housing with snap fastening for support rail DIN 46277

### Dimensional drawing

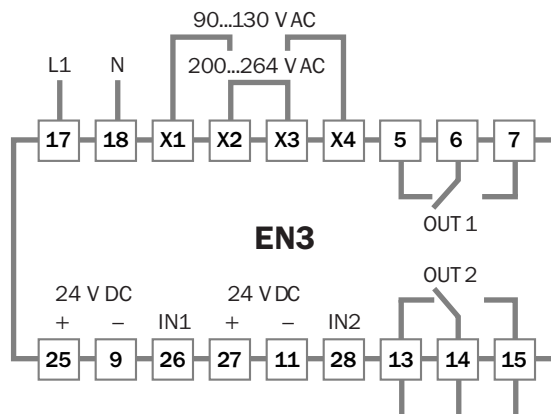


- 1 Fuse (250 V/0.25 A)
- 2 Display IN 1
- 3 Operating display
- 4 Display IN 2



### Connection diagram

EN 3

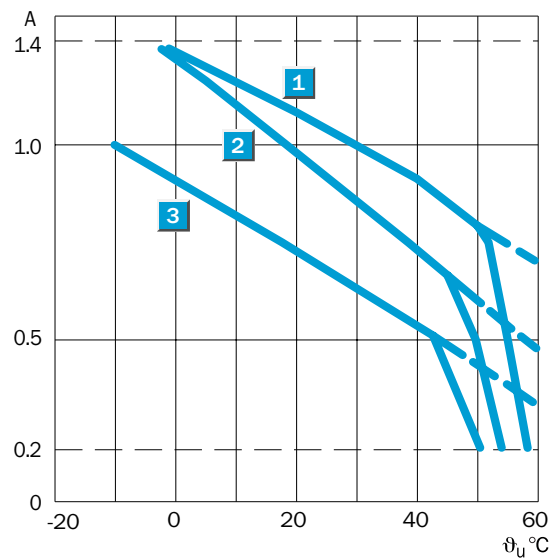


Technical data		EN 3									
<b>Supply voltage <math>V_s</math></b>	90...130 V AC or 200...264 V AC <sup>1)</sup> (can be reset using bridges)										
Mains frequency	48...62 Hz										
Power consumption	env. 40 VA										
<b>Outputs</b>											
Supply voltage for sensor	24 V DC $\pm$ 25 %										
Output current (total)	1.4 A in total, see load graph, output current										
Min. load	200 mA										
<b>Relay output</b>	5/6/7(OUT 1) and 13/14/15 (OUT 2)										
Switching voltage, max.	250 V AC										
Switching current, max.	2 A										
Switching frequency	20/s										
<b>Inputs for PNP, NPN<sup>2)</sup> and B</b>											
<b>sensor inputs</b>	26 (IN 1), 28 (IN 2)										
Input voltage	10...30 V DC										
HIGH	> 10 V DC										
LOW	< 6 V DC										
<b>VDE protection class<sup>1)</sup></b>	<input type="checkbox"/>										
<b>Enclosure rating</b>	IP 20										
<b>Ambient temperature</b>	Operation -25 °C...+55 °C Storage -40 °C...+70 °C										
<b>Shock load</b>	Complying with IEC 68										
<b>Weight</b>	Approx. 970 g										
<b>Housing material</b>	Plastic										

<sup>1)</sup> Delivery 200...264 V AC

<sup>2)</sup> External pull-up resistor  $\leq$  10 k $\Omega$  required for NPN variant

**Load curve, output current**



**Order information**

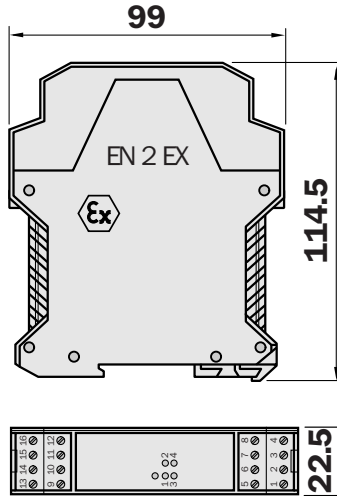
Type	Order no.
EN 3	6 009 692

# EN 2 Ex Switching amplifier

## Features

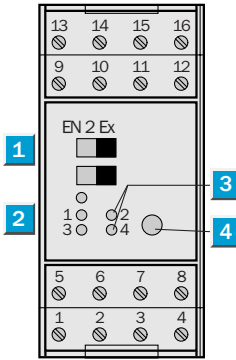
- **Ex II (1) G/D [EEx ia] IIC** according to Directive 94/9/EC (ATEX) with intrinsically safe inputs
- Reliable electrical isolation between input, output and supply voltage to VDE 0100 Part 410
- 2-channel each with one relay output 1 x u
- Invertible outputs
- Mounting on 35 mm (1.378 in.) DIN rails according to DIN EN 60715

## Dimensional drawing



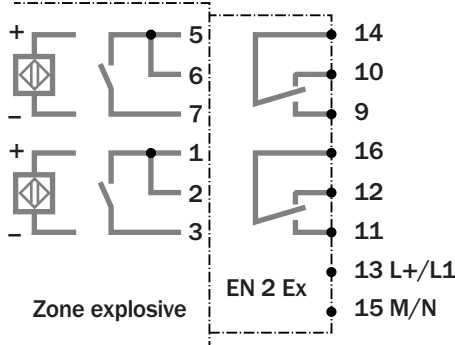
## Adjustments possible

All Types



- 1 Switches to reverse action. Switch in position I and contact in the input circuit closed, output active (ON). Switch in position II, output action inverted.
- 2 LED (red), cable monitoring indicator: The activation of the cable-break and cable short-circuit monitoring is only functional if a sensor/proximity switch to EN 60947-5-6 (NAMUR) or a mechanical contact with suitable resistance circuit as per the operating instructions is connected. This circuit monitors the input current and deactivates the output with input currents < 0.3 mA cable-break and > 6.5 mA short-circuit irrespective of setting for the direction of action.
- 3 LED (yellow), switch status indication: This LED is activated in parallel to the output.
- 4 LED (green), supply voltage indication.

## Connection diagram



Technical data		EN 2 Ex-	1	2	3							
Supply voltage $V_s$	120 V AC											
	230 V AC											
	24 V DC											
Power frequency	48 ... 62/s											
Power consumption per channel	Approx. 2.2 VA											
Power consumption, total	Approx. 0.7 W											
Inputs	For 1 or 2 sensors											
<b>No-load voltage</b>	8.5 V DC											
<b>EC-type examination certificate</b>	TÜV 03 ATEX 2346											
Output voltage $U_o$ , max.	$\leq 10.5$ V											
Output current $I_o$ , max.	$\leq 26$ mA											
Output power $P_o$ , max.	67 mW											
External capacity $C_a$ , max.	2.41 $\mu$ F											
External inductivity $L_a$ , max.	45 mH											
Ambient temperature $T_A$	Operation: $-20$ °C $\leq T_a \leq +60$ °C											
Switching points	$0 < 1.55$ mA											
	$1 > 1.75$ mA											
Short circuit current	$I \geq 8.5$ mA											
<b>Switching outputs</b> <sup>1)</sup>	1 relay per input: SPDT											
Switching voltage $U_{max.}$	250 V AC											
Switching current $I_{max.}$	5 A											
Switching power $P_{max.}$	100 VA											
<b>VDE protection class</b>	I											
<b>Enclosure rating</b>	IP 20											
<b>Ambient temperature <math>T_A</math></b>	Storage: $-25$ °C ... $+85$ °C											
<b>Weight</b>	Approx. 175 g											
<b>Housing material</b>	Plastic											

<sup>1)</sup> Provide suitable spark suppression for inductive or capacitive loads.

Transmission characteristics		Order information	
Active direction		<b>Type</b>	<b>Order no.</b>
(light-/dark-switching):	Can be changed over (see table)	EN 2 EX-1	6 010 459
Cable monitoring:	Can be switched off	EN 2EX-2	6 010 460
Max. switching frequency:	20/s	EN 2EX-3	6 009 944

#### Table of switching functions

Input		Active direction light/dark change-over switch on		Cable monitoring		Output status	
		I	II		Red LED	Relay	Yellow LED
No fault in input circuit	Contact open	Normal		as desired	off	dropped off	off
			Inverted	as desired	off	responded	on
	Contact closed	Normal		as desired	off	responded	on
			Inverted	as desired	off	dropped off	off
With fault in input circuit	Cable break	Normal		on	on	dropped off	off
			Inverted	on	on	dropped off	off
	Short-circuit	Normal		on	on	dropped off	off
			Inverted	on	on	dropped off	off
	Cable break	Normal		off	off	dropped off	off
			Inverted	off	off	responded	on
	Short-circuit	Normal		off	off	responded	on
			Inverted	off	off	dropped off	off