

IMPORTANT PRODUCT INFORMATION

READ THIS INFORMATION FIRST

Product: **FIP Bus Controller**
IC693BEM340-CD
IC693BEM341-AA
Firmware version: 3.01

General Description

This release adds support for the mode 30 configuration mode. See *New Features and Functionality* for details. This release is an upgrade to the previous release of the 1.0MHz version of the FIP Bus Controller (IC693BEM340). It is the first release that supports the 2.5MHz version (IC693BEM341) of the Bus Controller. Both Bus Controller versions are functionally identical. It is only the speed of the network that differs.

Compatibility

Field Upgrade Kit	44A739308-G03
CPU Requirements	The 1.0MHz version of the FIP Bus Controller (IC693BEM340) requires a Release 6.5 version or greater of any IC693 PLC CPU. The 2.5MHz version of the FIP Bus Controller (IC693BEM341) requires a Release 8.10 version or greater.
Configuration Requirements	This release of the FIP Bus Controller requires the Hand-Held Programmer (HHP), windows programming software IC641CTLxxx Release 2.0 or later, or the FBCLOAD configuration utility to configure it for proper network operation. The IC641SWPxxx programming software cannot configure this version of the FIP Bus Controller.
Compatibility	For Mode 11 and Mode 20, this release requires Version 2.0 of the FBCLOAD external configuration loader. Previous versions of FBCLOAD are incompatible. For Mode 20, the TIME POSIX COMV format has been changed according to the FIP Device Manager Version 4 specification. For Mode 20, a new value for <i>subscriber type 2</i> is used in the time stamping messages.

New Features and Functionality

Enhanced HHP display All modes	The firmware revision is now displayed.
Time stamping Mode 20 only	This release supports time stamping of up to 1024 Boolean values. The first messages chronology number is 1.

- New mode 30 added** This release adds the new mode 30. This mode is an extension of the mode 11 and adds the following features:
- Mode 30 only
- Redundancy
 - Management of slave devices (I/O nests, drives)
 - Monitoring of slave devices (I/O nests, drives, PLCs, control stations)
 - Time stamping of Boolean values
 - Messaging services
 - MPS synchronous exchanges
 - Reception of the external configuration through the FIP network.

Problems Fixed in this Release

- Loss of FBC module** This release corrects a problem which produced a “Loss of Module” fault reported by the PLC CPU for the FBC30. This problem occurred in only some rare cases.
- All modes
- The state of the Application was incorrectly determined** This release corrects a problem where the state of the PLC application was determined incorrectly. This problem occurred when the scan time of the application was near or equal to the value of the configuration parameter *max time*.
- All modes
- Validity bits for input COMVs were not always set** This release correctly sets the validity bits for input COMVs when the COMVs become invalid. This problem occurred in the FBC30 *Running Unlocked* state.
- Mode 10 only
- Produced COMVs were not updated correctly** This release ensures that the produced COMVs are not updated until the PLC application provides the output data.
- Mode 11 only
- Consumed COMVs were not updated when PLC application was stopped** This release continues to update the input COMVs even when the PLC application is stopped. This action ensures that the input data and validity bits are up-to-date when the PLC application starts again.
- Mode 11 only
- Time stamping message incorrectly received** This release corrects a problem in the management of the reception of the SPV_CG message.
- Mode 20 only